1st International Conference on
Artificial Intelligence, Security and Communications (AISC) 2022

25-26th November 2022

In Collaboration with

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https://www.springer.com/journal/42979

Session - 1

Artificial Intelligence and Its Applications

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All the time of article submission, please follow the given steps:

Step-1: Only Submit your **ABSTRACT** through Easychair and wait for **acceptance notification** for the abstract. https://easychair.org/my/conference?conf=aisc2022

Step-2: Follow the instruction to **submit the full paper in the acceptance mail**.

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This AISC 2022 conference will provide an excellent international forum for sharing knowledge and results in the theory and applications of Artificial Intelligence, Security, and Communication, Machine Learning, Deep Learning, Computer Networks, the Internet of Things, Data Science, and similar CSE fields. Original papers are invited on Networks, the Internet of Things, and security. The AISC 2022 conference aims to provide a platform for researchers and practitioners from academia and industry to meet and share cutting-edge development in the field of Innovation and Creativity. http://aisc2022.iaasse.org/workshops.html

Topics of interest include, but are not limited to, the following

Artificial Intelligence and Its Applications

- Artificial Intelligence and Automation,
- Data Science and Big Data,
- Blockchain and Security,
- AI for Space Sciences and Applications
- Cryptography and Quantum Computing,
- Data Communications and Networks, 5G, 6G, Social Networks,
- Internet of Things and Its Applications, IIoT 4.0 and Embedded Systems,
- Computational Biology and Bio-Medical
- Informatics,
- AI for Neuroscience Applications

- Image, Signal Processing, and Multimedia,
- Cognitive Science and Algorithm Design
- Augmented Reality and Virtual Reality
- Cloud computing and Soft, Fog, Edge, Grid Computing,
- Internet and Web Applications.
- E-Commerce, E-Governance, and Game
- Theory and Applications
- Experimental Simulation and Modelling
- Techniques
- Intelligent Systems Architecture and advanced Holographic Projections

* Important Dates / Deadlines / FEES *

Submission of **Abstracts** : **August 31, 2022**
Submission of **Full Paper** : **September 30, 2022**
Notification of **Acceptance** : **October 15, 2022**
Early Bird Registration : **Oct 16 – Nov 08 , 2022**
Oral Presentation of selected works : **November 25-26, 2022**
Submission of **Final paper** : **December 20, 2022**

Category	Early Bird: on or before Oct 25, 2022		Regular Registration: Oct 25, 2022 - Till Oct 31, 2022		Spot Registration: Nov 25, 2022	
	IAASSE Members	Non Members	IAASSE Members	Non Members	IAASSE Members	Non Members
Full-Time Students	70.00	95.00	95.00	120.00	120.00	145.00
Faculty/ Research Scholars	95.00	120.00	120.00	145.00	145.00	170.00
Industry	115.00	140.00	140.00	165.00	165.00	190.00
Attendees	25.00	35.00	35.00	45.00	45.00	55.00

** About Session Chairs **

Dr. Priyanka Mishra was born in Jaipur, India, in 1980. She received her B.E. degree in Computer Engineering from the Modi College of Engineering & Technology, Laxmangarh under the University of Rajasthan, India, in 2003, and her M.Tech. Degree in Computer Science in 2006. She received a Ph.D. degree in Computer Science & Engineering from the National Institute of Technology (NIT) Hamirpur, India, in 2016. She is presently working as Assistant Professor (Gr. II) in the Dept of CSE, IIITK, MNIT Campus, Jaipur. Dr. Mishra filed 1 International PCT Patent application, secured 1 Australian patent Grant, and also filed 3 Indian Patents among them, all published in the Indian Patent Journal, Govt. of India, and secured 1 Australian Patent Grant. She secured 1 Copyright which was granted by the Copyright Office, Government of India, and Dr. Misra also wrote Two (2) Textbooks including 1 e-book available in Worldwide Best Bookseller Stores, and also published 5 Book chapters, and honored 2 International and National Prestigious awards from her qualitative services for research fields. Dr. Mishra is a life member of CSI, EII, ISTE, and a Professional Member of ACM. She is also a Member of IEEE, CSTA, IAENG, SDIWC, SCIEI, IRED, Rotary Club Jaipur, and UACEE. She is a Member of the Editorial Board of many international journals viz. IJRDES, IJCSE, IJCST, IJCTT. Her field of interest includes machine learning, IoT, distributed artificial intelligence, identity management, trust and reputation modeling, and network security. She is a life member of CSI, ISTE, and IEI. Her papers appeared in various International Academic Journals and conferences such as IEEE, Springer, Elsevier, etc. Her research interest includes Trust & Reputation Management, Identity Management, and Network security (network perimeter defense, wired/wireless/mobile network security, semantics/trust-level, and end-user interaction security.). Dr. Priyanka has intensive editorial experience as an Editor for the International Journal of Research and Development in Engineering Sciences (IJRDES-2582- 4201)).

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Bosubabu Sambana has been working as an Associate Professor in the Department of CSE at Lendi Institute of Engineering and Technology (Autonomous), Vizianagaram affiliated with Jawaharlal Nehru Technological University Kakinada, Andhra Pradesh, India. He completed his Bachelor of Science (B.Sc) from Andhra University. He completed a Master of Computer Applications (MCA) and Master of Technology – CSE (M.Tech) from JNTU Kakinada. He completed AMIE (CSE) from the Institute of Engineers of India (IEI), Kolkata. He completed Post Graduate Diploma in Cloud Computing (PGDCC) from the National Institute of Electronics & Information Technology, Calicut.

He is pursuing a Doctor of Philosophy (Ph.D.) (Artificial Intelligence with Neuroscience) in Computer Science and Engineering from the Indian Institute of Information Technology (IIIT) Kota, Rajasthan.

He submitted 1 Internet Draft for RFC (Request For Comments) for Internet Protocol version 16 (IPv16) to IETF. He filed 4 International Patents, which were published in WIPO PCT, and also filed 38 Indian Patents including 10 Designs, and Secured 1 Indian Patent Grant too, among them, all are published in Indian Intellectual Property Rights Journal (Patents, Design, Trademarks, GI), Govt. of India, and secured 3 Australian Patent Grants. He secured 1 USA Copyrights Grant from the Copyright Office of the USA and Secured 10 Copyrights, which were granted by the Copyright Office, Government of India, and also 1 Indian Trademark granted too. He also wrote four (4) Textbooks, and also 3 e-Books available in Worldwide Best Bookseller Stores, and also published 5 Book chapters in IEEE, Springer Nature, and AAP - CRC Press & Taylor and Francis Group. He published 58+ Research papers in various reputed (UGC approved / Scopus/ SCI / Web of Science) International and National Journals, magazines, and conferences. He published 5 Preprints in Cornell University and Easy Chair Publications.

Mr. Bosubabu has intensive editorial experience (Editor-in-chief for International Journal of Research and Development in Engineering Sciences (IJRDES-2582- 4201)). He is honored with the " Outstanding Researcher Award 2019 " by the International Institution of Organized Research, " Young Scientist Award 2020 " by the South Asian Educational Research Awards Society, and "Outstanding Scientist Award 2020" by VDGOOD Professional Association. He acts as Organizing Chair for International Conferences of ICMLBDA 2022 and MISP 2022 and conducted Many Workshops, Seminars, etc. He participated in 18 international and 3 national conferences and 14 international and National workshops and 12 FDPs. He secured 6 Certifications from NPTEL – IISc / IITs and 2 online courses from IIRS-ISRO and NIELIT Chennai. He has more than ten years of experience in teaching and research and also has good knowledge of Artificial Intelligence, Astronomy, Cosmology, Space Research, Future Internet Architecture & Technologies, Blockchain, Cloud Computing, and Internet of Things along with academic subjects, etc. He guided 8 UG and 2 PG academic projects. Currently, he is an active member of IEEE, IAENG, AMIE, ISA, CSI, and the Indian Science Congress.

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Dr. Rahul Pandya completed his M.Tech. from the Electrical Engineering Department, Indian Institute of Technology, Delhi, New Delhi, in 2010. He completed his Ph.D. from Bharti School of Telecommunication, Indian Institute of Technology, Delhi, in 2014. He worked as the Sr. Network Design Engineer in the Optical Networking Industry, Infinera Pvt. Ltd., Bangalore, from 2014 to 2018. Later, from 2018 to 2020, he worked as the Assistant Professor, ECE Department, National Institute of Technology, Warangal. Currently, he is working Electrical Engineering department at the Indian Institute of Technology, Dharwad. His research areas are Wireless Communication, Optical Communication, Optical Networks, Computer Networks, Machine Learning, and Artificial Intelligence. He is working on multiple projects, such as SERB, SPARC, SGNF, and RSM.
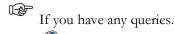
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Dr. Ghanapriya Singh is working as Asst. Professor in the Department of Electronics Engineering at the National Institute of Technology, Uttarakhand since 2013. She received her Ph.D. degree in Signal Processing from the Indian Institute of Technology (IIT) Delhi. During her research at IIT., she was working on a project with STMicroelectronics, Santa Clara, California, USA. Her research work done at IIT was also showcased at CES, the USA which is the largest consumer electronics show. She is an inventor on 5 US patents (all granted) and a European patent. She is an Investigator in the project funded by the Ministry of Electronics and Information Technology, Govt. of India. She has authored in high-impact factor SCI-indexed journals. Her current research interests include context awareness, image processing, speech processing, and signal processing for IoT.

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Thank You